

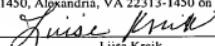


1 FW
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of)
Redeker et al.) Examiner: Dang, Trung Q.
Application No. 10/734,704) Art Unit: 2823
Filed: December 12, 2003) Docket No. LAM2P461
For: METHOD AND APPARATUS FOR) Date: March 25, 2005
SEMICONDUCTOR WAFER PLANARIZATION)

CERTIFICATE OF MAILING
I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on March 25, 2005.

Signed: 
Liisa Kreik

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
Alexandria, VA 22313-1450

Dear Sir:

Please note that this response to the Restriction Requirement dated February 24, 2005, is being submitted within the 30 day period for reply extending to March 28, 2005. In response to the Restriction Requirement, Applicants elect without traverse Group I (claims 1-9), to prosecute in the above-identified patent application. Should the Examiner have any questions concerning this matter, the undersigned can be reached at the telephone number below.

Respectfully submitted,
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